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**Han**

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(54) **HIGH VOLTAGE JUNCTION FIELD EFFECT TRANSISTOR**

(71) Applicant: **CSMC TECHNOLOGIES FAB1 CO., LTD.**, Jiangsu (CN)

(72) Inventor: **Guangtao Han**, Jiangsu (CN)

(73) Assignee: **CSMC TECHNOLOGIES FAB1 CO., LTD.**, Jiangsu (CN)

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See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

5,910,664 A \* 6/1999 Ajit ..... H01L 27/0716  
257/139

6,037,238 A 3/2000 Chang et al.  
(Continued)

FOREIGN PATENT DOCUMENTS

CN 101901805 A 12/2010  
CN 101969072 A 2/2011

(Continued)

OTHER PUBLICATIONS

International Search Report dated Sep. 19, 2013.  
Written Opinion of the International Searching Authority dated Sep. 19, 2013.

*Primary Examiner* — Zandra Smith

*Assistant Examiner* — Lawrence Tynes, Jr.

(74) *Attorney, Agent, or Firm* — Polsinelli PC

(57) **ABSTRACT**

The present invention discloses a high voltage JFET. The high voltage JFET includes a second conductivity type drift region located on the first conductivity type epitaxial layer; a second conductivity type drain heavily doped region located in the second conductivity type drift region; a drain terminal oxygen region located on the second conductivity type drift region and at a side of the second conductivity type drain heavily doped region; a first conductivity type well region located at a side of the second conductivity type drift region; a second conductivity type source heavily doped region and a first conductivity type gate heavily doped region located on the first conductivity type well region, and a gate source terminal oxygen region; a second conductivity type channel layer located between the second conductivity type source heavily doped region and the second conductivity type drift region; a dielectric layer and a field electrode plate located on the second conductivity type channel layer. Wherein a drain electrode electrically is led out from the second conductivity type drain heavily doped region; a

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